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L	Bult, Klaas and Buchwald, Aaron, "An Embedded 240-mW 10-b 50-MS/s CMOS ADC in 1-mm²," IEEE Journal of Solid-State Circuits, IEEE, Vol. 32, No. 12, December 1997, pp. 1887-1895.									
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L	Choi, M. and Abidi, A., "A 6-b 1.3-Gsample/s A/D Converter in 0.35-\(\mu\)m CMOS," IEEE Journal of Solid-State Circuits, IEEE, Vol. 36, No. 12, December 2001, pp. 1847-1858.										
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L	AS	<u>3</u>	Geelen, G. State Circ	, "A 6b 1.1GS; uits Conferenc	ample/s CMOS A/D Conve ce, IEEE, 2001, pp. 12	erter," IEEE I 8-129 and 438	nternation . No mol	al solid- 1th/pde h			
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L	AT 4	4	Ito, M. et Solid-State	al., "A 10 bi e Circuits, IE	it 20 MS/s 3 V Sup EEE, Vol. 29, No.	ply CMOS A 12, Decemb	/D Conver er 1994,	ter," <i>IEEE</i> pp. 1531-1	3 Journal of .536.		
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L	AS	<u>5</u>	Kusumoto, Journal of 1206.	K. et al., "A Solid-State (10-b 20-MHz 30-mW Pipelin Circuits, IEEE, Vol. 28, N	ned Int	erpol , Dece	ating CMOS	S ADC," <i>IEEE</i> , pp. 1200-		
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L	AS	<u>6</u>	Nagaraj, K End," <i>IEEE</i> pp. 1056-1	Journal of Sc	icient 6-Bit A/D blid-State Circuit	Converter U	sing a 1 1. 34, N	-Bit Fold o. 8, Aug	ing Front ust 1999,		
1	AT	<u>6</u>	Nagaraj, K. et al., "A Dual-Mode 700-Msamples/s 6-bit 200-Msamples/s 7-bit A/D Converter in a 0.25-\(\mu\)m Digital CMOS," <i>IEEE Journal of Solid-State Circuits</i> , IEEE, Vol. 35, No. 12, December 2000, pp. 1760-1768.								
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L	AS	<u>7</u>	dB SFDR,"	al., "A 3.3- IEEE Journal (1769-1780.	V 12	2-b 50-MS/s A/D Converter Solid-State Circuits, IEE	in E, \	0.6-μ Vol. 3	m CMOS wi 5, No. 12	th over 80- , December	
S	AT	<u> 2</u>	Song, W-C. et al., "A 10-b 20-Msample/s Low-Power CMOS ADC," IEEE Journal of Solid-State Circuits, IEEE, Vol. 30, No. 5, May 1995, pp. 514-521.								
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L	AT .	<u>8</u>	0.35-μm CM	an der Ploeg, H. and Remmers, R., "A 3.3-V, 10-b 25-Msample/s Two-Step ADC in .35-µm CMOS," IEEE Journal of Solid-State Circuits, IEEE, Vol. 34, No. 12, ecember 1999, pp. 1803-1811.									
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